

Figure 1

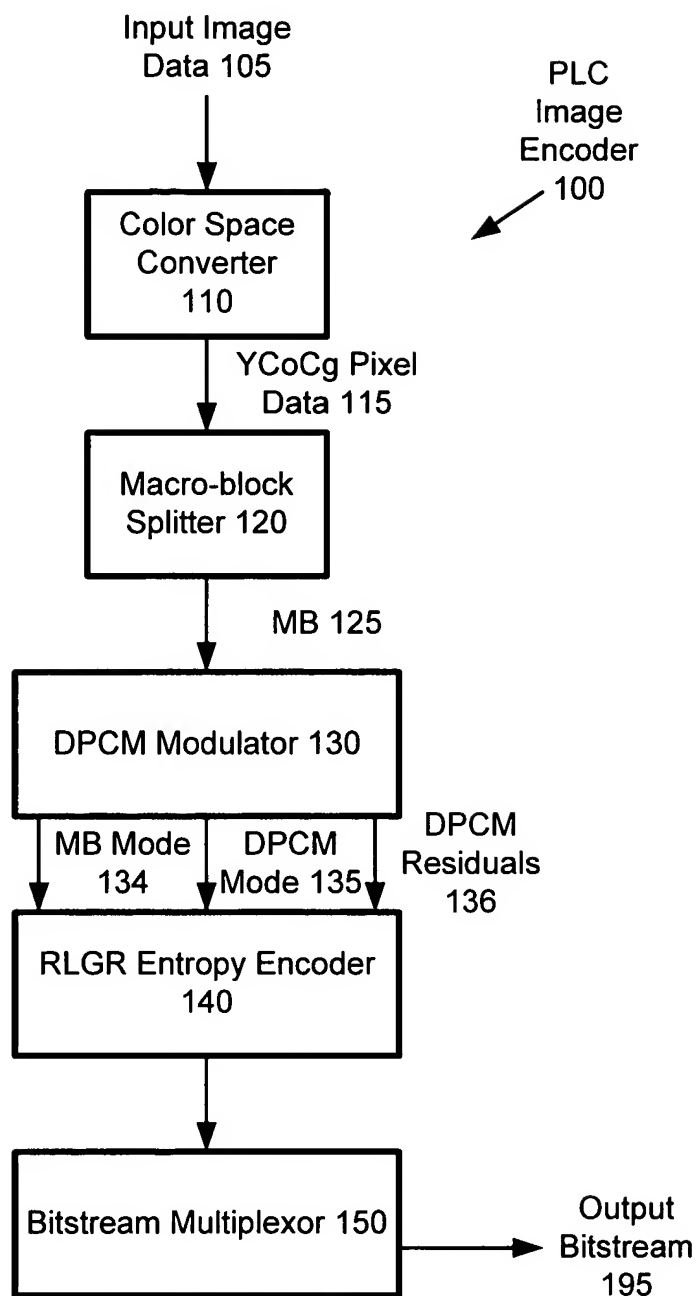


Figure 2

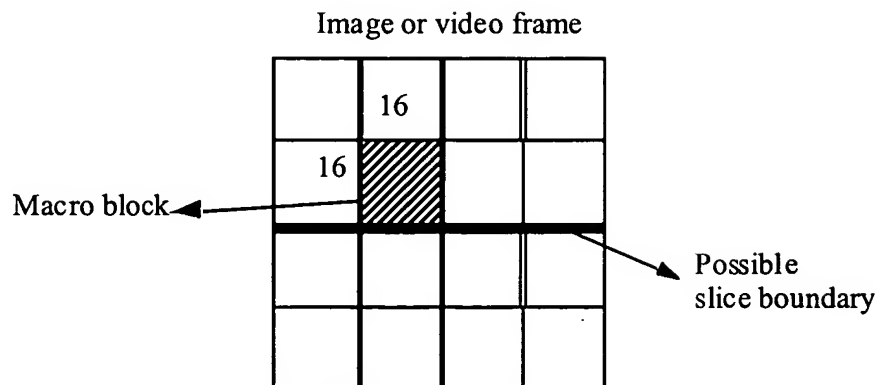


Figure 3

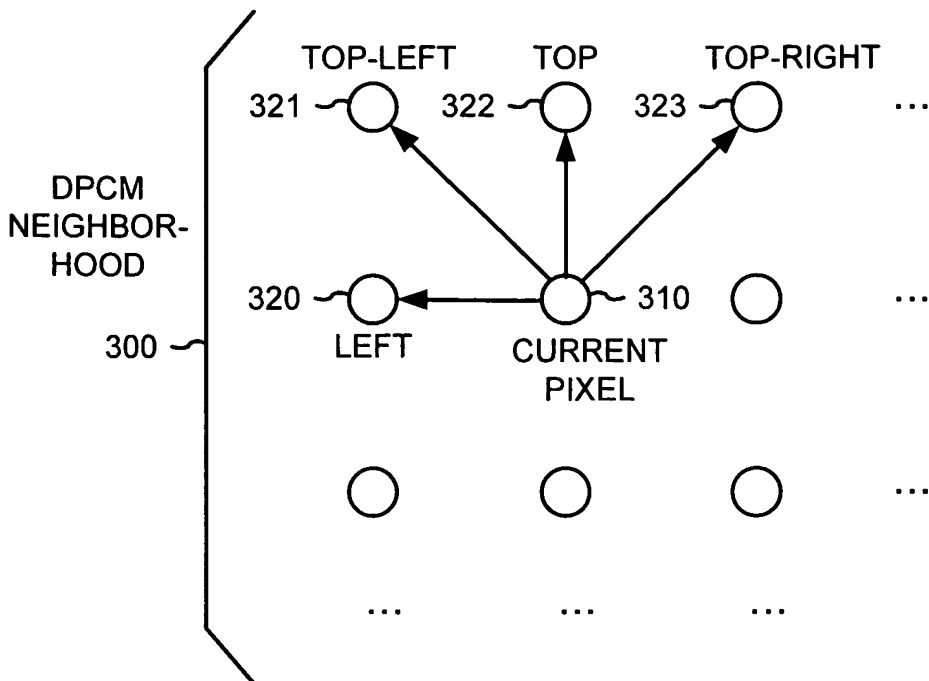
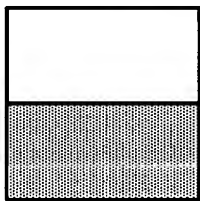


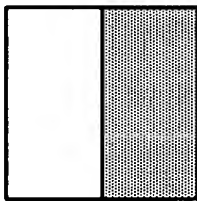
Figure 4

DPCM MODES

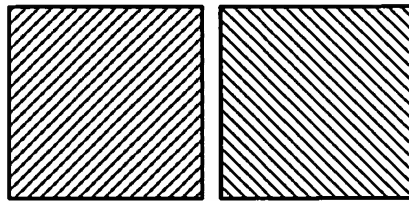
MODE 1: HORIZONTAL
MAJOR EDGE



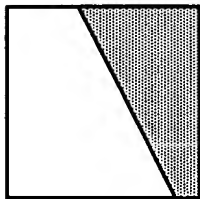
MODE 2: VERTICAL
MAJOR EDGE



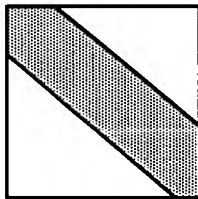
MODE 3: RAMP
DIAGONAL EDGES



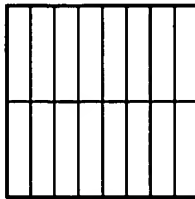
MODE 4:
DIAGONAL
RAMP EDGE



MODE 5:
DIAGONAL
BAND



MODE 6: BANDED
HORIZONTAL
RAMPS



MODE 7:
EDGE

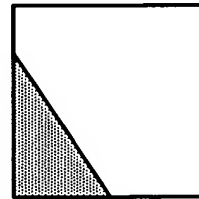
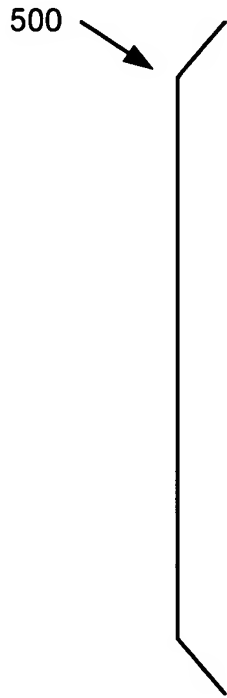


Figure 5

500



```
PLC (IN ImageBand i, OUT BitStream b)
for all Macroblocks do
    find and apply the best DPCM mode
    if MB is flat then continue           // skip rest of for block
        RLGR code the DPCM residuals
end for
End PLC
```

Figure 6

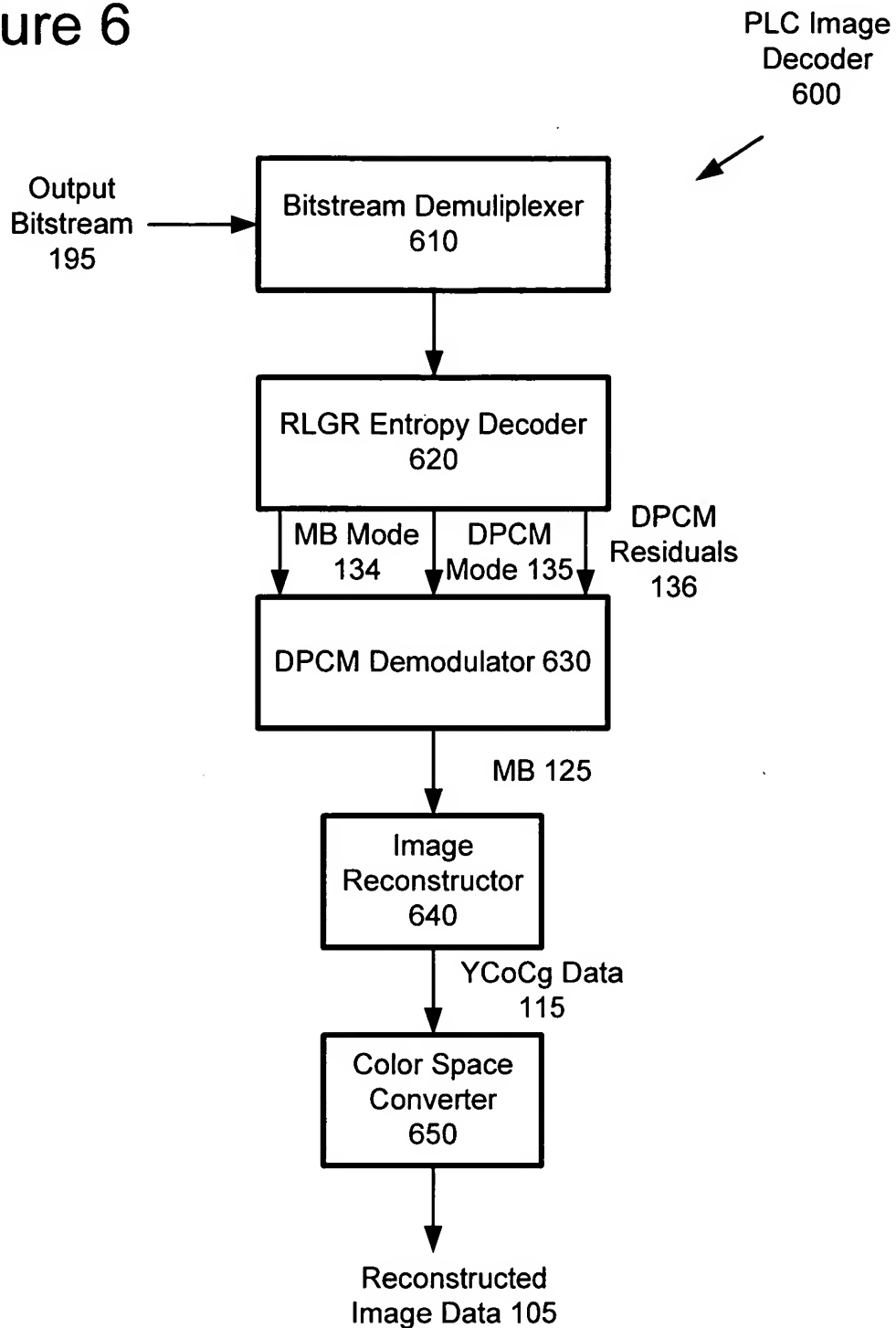


Figure 7

